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jc844 U.S. PTO

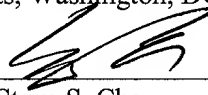
Attorney Docket No. P2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

jc834 U.S. PTO
09/591245
06/09/00

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on June 9, 2000 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR § 1.10, Mailing Label Number EJ 391268835 US, addressed to Assistant Commissioner of Patents, Washington, DC 20231.


Steve S. Cha

UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR § 1.53(b))

Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

Transmitted herewith for filing is the patent application under 37 CFR § 1.53(b) in the name of inventor(s) of:

JUNG-GI KIM

For: PROGRAM UPGRADING APPARATUS AND METHOD FOR FIRMWARE BOARD

Enclosed are:

Application Elements:

- ☒ 2 copies each of 6 sheets of informal drawing(s).
- ☒ Assignment of the invention to: SAMSUNG ELECTRONIC CO., LTD.
- ☒ Specification, Claims, and Abstract: Nr. of sheets: 22
- ☒ A copy of combined declaration and power of attorney:

Accompanying Application Parts:

- ☒ A check* 1752 in the amount of \$840.00 for filing fee
- ☒ A Return Receipt Postcard
- ☒ Assignment and Assignment Recordation Cover Sheet (recording fee of \$40.00 enclosed)
- ☒ Claim of priority under 35 U.S.C. §119 and a copy of certified Korean Patent Application.
- ☐ Information disclosure statements: Nr. of sheets: 3

09591245 "060900"

- ☐ Preliminary Amendment
☐ Small Entity Statement(s)
☐ Other:

The filing fee has been calculated as shown below (37 CFR §1.16):

For:	No. Filed	No.Extra	Rate(Small Entity)	Fee(Small Entity)
Basic Fee	20 claims			\$ 690.00
Total Claims	24 claims – 20 =	4	\$18(\$9)/claim	\$ 72.00
Independent Claims	4 claims – 3 =	0	\$78(39)/claim	\$ 78.00
Multiple Dep.Claims			\$130(65)/claim	\$ 0.00
Total Filing Fee				\$ 840.00

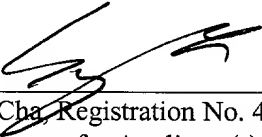
General Authorization for Petition for Extension of Time (37 CFR §1.136)

Applicant hereby make and generally authorize any Petitions for Extensions of Time as may be needed for any subsequent filings.

Should the enclosed check become lost or detached from the file, Assistant Commissioner is authorized to charge our Deposit Account No. 50-1005 and advise the undersigned attorney accordingly. Also, should the enclosed check be deemed to be deficient or excessive in payment, the Commissioner is authorized to charge or credit our deposit account and notify the undersigned attorney of any such transaction.

In view of the above, it is respectfully requested that this application be accorded a filing date pursuant to 37 C.F.R. §1.53(b).

Mailed: June 9, 2000


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Customer No:022491



022491

PATENT TRADEMARK OFFICE

APPARATUS AND METHOD OF UPGRADING PROGRAM OF FIRMWARE

BOARD

CLAIM OF PRIORITY

5 This application makes reference to, incorporates the same herein, and claims all
benefits accruing under 35 U.S.C. Section 119 from an application for PROGRAM
UPGRADING APPARATUS AND METHOD FOR FIRMWARE BOARD filed earlier in
the Korean Industrial Property Office on June 9, 1999 and there duly assigned Serial No.
21353/1999.

BACKGROUND OF THE INVENTION

1. Field of the Invention

10 The present invention relates generally to a firmware board having an incorporated
function program stored in a non-volatile memory. More particularly, the present invention
15 relates to an apparatus and method for upgrading the function program of the firmware board.

2. Description of the Related Art

Typically, a firmware board is provided with an erasable and programmable read only
memory (EPROM), an electrical erasable and programmable read only memory (EEPROM),
and other non-volatile memory to store pre-programmed boost-up data (i.e. firmware) relevant

to establish information or configuration therein. The EPROM, which stores the program, is manufactured using an EPROM writer, then the manufactured EPROM is superimposed on the nonvolatile memory of the firmware board to drive the firmware board. As occasion demands, the program for driving the firmware board is upgraded.

However, the prior art system of upgrading the program for driving the firmware board has drawbacks. The first drawback is that it requires additional hardware, i.e., the EPROM writer itself. The second drawback is that it is inefficient and inconvenient as the EPROM already packaged on the firmware board must be physically removed from the backbone while the power of the firmware board is turned off to replace the old EPROM with a newly manufactured EPROM.

SUMMARY OF THE INVENTION

Accordingly, the present invention has been made in an effort to solve the problems occurring in the related art, and therefore, an object of the present invention is to provide an apparatus and method for upgrading the program for driving a firmware board without using a
5 separate tool, such as the EPROM writer.

It is another object of the present invention to provide an apparatus and method for upgrading the program for driving a firmware board that can eliminate the intervention to the hardware by the end user when upgrading the program.

10 It is still another object of the present invention to provide an apparatus and method for upgrading the program for driving a firmware board so that the program can be more easily upgraded by simply downloading new firmware data to acquire new capabilities.

In order to achieve the above objects in the embodiments of the present invention, the program of the firmware board can be upgraded by the built-in program and protocol without
15 turning off the power of the firmware board and without using a separate hardware tool, such as an EPROM writer.

According to the present invention, a flash memory is provided on a firmware board to store the program for production. A host computer creates a file for production by converting

an execution file, prepared in advance, into the file for production. A personal computer (PC) receives the production file from the host computer by downloading therefrom, and the PC stores the production file in the corresponding region of the flash memory using a production-processing program of the firmware board.

BRIEF DESCRIPTION OF THE DRAWINGS

The above objects and advantages of the present invention will become more apparent by describing in detail the preferred embodiment thereof with reference to the attached drawings in which:

FIG. 1 is a block diagram illustrating the construction of an apparatus for upgrading the program for driving the firmware board according to the present invention;

FIG. 2 is a flowchart illustrating a file creating process according to the present invention;

FIG. 3 is a view explaining the structure of an .INI data file used for creating the file for production according to the present invention;

FIG. 4 is a view illustrating the structure of the file for production that is created according to the present invention;

FIG. 5 is a view illustrating the structure of a header of the file for production created according to the present invention; and,

FIG. 6 is a flowchart illustrating the process of transmitting the file for production onto the firmware board and for storing the file for production in the corresponding region of a flash memory according to the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

In the following description, for purposes of explanation rather than limitation, specific details are set forth such as the particular architecture, interfaces, techniques, etc., in order to provide a thorough understanding of the present invention. However, it will be apparent to those skilled in the art that the present invention may be practiced in other embodiments that depart from these specific details. For the purpose of clarity, detailed descriptions of well-known devices, circuits, and methods are omitted so as not to obscure the description of the present invention with unnecessary detail.

FIG. 1 depicts a block diagram illustrating the construction of an apparatus according to the present invention for upgrading the program for driving the firmware board. The upgrading apparatus employs a flash memory 124 in which a data read/write operation is performed utilizing software for storing the program with new capabilities onto a firmware board 120, instead of replacing the EPROM as in the prior art.

With reference to FIG. 1, a host computer 100, which operates under the control of an operating system, converts an execution file prepared in advance by a programmer into a file for production by converting the object codes into an appropriate format to be stored in the firmware board. Here, the host computer 100 can be a UNIX system. The execution file includes program object codes for upgrading the program that are used for driving the firmware board. The host computer 100 creates the file for production using a software

known as a “SIGX2B” execution program to convert the program object codes into the file for production.

It should be noted that within this disclosure, the term personal computer (PC) is meant broadly to refer to any type of programmable terminal – i.e., notebook PC, cellular phones, digital cameras, or other small-size information apparatus.

A personal computer (PC) 110 downloads the production file created by the host computer 100 and stores the production file in a corresponding region of the flash memory 124 of the firmware board 120. In order to actuate the transfer, the PC 110 is provided with a communication (COM) port 112 for loading the production file to the firmware board 120. The communication port 112 is connected to the input/output port 122 of the firmware board 120 through an RS-232C line. Here, the terminal emulator of the PC 110 performs the transmission of the file through the communication port 112.

The firmware board 120 is provided with a non-volatile memory, flash memory 124, in which the data read/write is possible using software and stores the drive program therein. That is, the flash memory 124 stores the production-processing program, which is stored initially in the flash memory 124 using an EPROM writer, and thereafter, the program production/upgrading is performed in a manner that any program is stored in the flash memory 124 using the production-processing program. The production-processing program receives the file which was converted to the production format using the “SIGX2B” program

from the PC 110 and saves it onto the flash memory 124. Then, after upgrading with the new program, the boost-up program boots this new file stored in the flash memory 124. Accordingly, the PC 110 can upgrade the program stored in the flash memory 124 using the production-processing program.

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Moreover, in the embodiment of the present invention, the firmware board 120 includes a Dynamic Random Access Memory (DRAM) 126. If the file for production created by the host computer 100 corresponds to the file for upgrading the production-processing program, the production process is performed by copying the production-processing program from the flash memory 124 onto the DRAM to perform the upgrading process therein, then the upgraded production-processing program is stored back into to the flash memory 124 again. For upgrading the operation, the embodiment is provided the DRAM 126. It should be noted that a Static Random Access Memory (SDRAM) can be also used.

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FIG. 2 is a flowchart illustrating a process of creating a file for a production performed by the host computer 100 according to the present invention. The following will be a general discussion of the present invention in the context of a software related to the PC and the firmware. It is to be understood that the software and hardware can take many different configurations such as is within the skill of those skilled in the art.

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With reference to FIG. 2, the host computer 100 creates an execution file by compiling/linking operating programs, MODULE 1, HEX,.....,MODULE n, HEX, for

respective modules in step 210. Then, the host computer 100 creates database files, DATA 1, BIN,, DATA n, BIN, in step 220. Thereafter, in step 230, the host computer 100 converts the respective execution files created in step 220 into files for production, PGM 1, 000,, PGM n, 000, using a "SIGX2B.EXE", which is a tool for creating the files for
5 production. At this time, the files for production are created after a file of ".ITC.INI" is executed. However, the ITC.INI is a normal test file which can be produced using any editor program and is created before the production file is made. The ".ITC.INI" is an input data file for "SIGX2B.EXE" and notifies the "SIGX2B.EXE" with the configuration and method for making a production file. Thus, the ".ITC.INI" file is a file which clarifies the production
10 guide_to the production file and includes information on storage regions, booting, etc., of the flash memory 124. This information is contained in the header portion of the production file. Input parameters can be set by an operator using a single, .INI file to specify application parameters and defines the starting parameters for the system. That is, the .INI file specifies the application, services, driver letter, program, install file, set-up program, memory space,
15 user name, and/or message type. In step 240, the file for production created in step 230 is downloaded to the PC 110.

As described above, the "SIGX2B" execution file is a tool for creating S-record files and binary files as production files. When creating the production files, it follows instructions recorded in the .INI file. Thus, by executing the "SIGX2B package name .INI", the files for
20 production are created.

FIG. 3 is a view explaining the structure of the .INI data file used for creating the production file according to the present invention. This .INI data file is an input text file of the “SIGX2B” execution file. A list of files which constitute a package for production and commands for the package construction are recorded in the .INI data file.

INI File Parameter

1. Reference number, 300, denotes a region where information related to the firmware board is stored.
2. Reference number, 301, denotes an information tag related to the firmware board.
3. Reference number, 302, denotes a package identifier of a fixed value, i.e., the name of the firmware board.
4. Reference numbers, 310, 320, and 330, denote regions where information related to respective modules/components of the firmware board is stored.

As information related to the respective modules can be stored in the same manner, only the first module 310 will be explained as an illustrative example hereinafter.

5. The reference number, 311, denotes an information tag related to a module (MODULE).
6. The reference number, 312, denotes a file name of modules (File Name) which constitutes a package.

7. The reference number, 313, denotes the booting state of the file (LoadFlag). For example, “0” represents that the file has not been booted and “1” represents that the file has been booted.
8. The reference number, 314, represents the type of file stored in the flash memory 124. For example, “0” represents that the file is of a non-compressed type and “1” represents that the file is of a compressed type.
9. The reference number, 315, represents the type of an input file (BINflag). For example, “0” represents an S-record file and “1” represents a binary file.
10. The reference number, 316, represents the position (unit of 64Kbytes) of the flash memory 124 where the file is stored (SaveAddr).
11. The reference number, 317, represents the maximum size of a resultant file (SaveSize). If the resultant file exceeds 64Kbytes, it is created as “*.000, *.001”.

FIG. 4 is a view illustrating the structure of the production file created according to the present invention. The production file is created through the process as shown in FIG. 2.

With reference to FIG. 4, the file for production comprises a file header 410, and file content 420. The file header 410 is composed of 32 bytes, and the file content 420 is composed of 64Kbytes – 32bytes. The production file can have the size of 128Kbytes at maximum. The file content 420 can be a compressed type according to ZipFlag of the .INI file.

FIG. 5 is a view illustrating the structure of the header of the production file created according to the present invention (i.e., the structure of the file header 410 of FIG. 4).

With reference to FIG. 5, the header 410 of the file for production comprises “ID (00h~03h)”, “ATTRIB (03h~04h)”, “FILE NAME (04h~0Fh)”, “BOOTING ADDRESS (10h~14h)”, “BOOTING SIZE (14h~18h)”, “SAVE ADDRESS (18h~1Ch)”, and “SAVE SIZE (1Ch~1Fh)”.

Header Parameters

1. The “ID” represents the package identifier and indicates the ID of the .INI file.
2. The “ATTRIB” represents the attributes of the file that exist in the .INI file. At this time, “bit 0” represents the booting state and “bit 1” represents the compression state.
3. The “FILE NAME” represents the file name of a module that exists in the .INI file.
4. The “BOOTING ADDRESS” represents the position where the corresponding file is booted and extracted from the execution file.
5. The “BOOTING SIZE” represents the booting size of the corresponding file that is extracted from the execution file.
6. The “SAVE ADDRESS” represents the position of the flash memory 124 where the content of the corresponding file is stored.
7. The “SAVE SIZE” represents the size of the content of the corresponding file.

FIG. 6 is a flowchart illustrating the process of transmitting the production file created according to the present invention to the firmware board and for storing the production file in the corresponding region of the flash memory. This process is performed by the terminal emulator of the PC and the production-processing program of the firmware board 120 using the production file that is downloaded from the host computer 100.

If an operator inputs a command through the PC 110 to transmit the file for production, i.e., if the operator enters a return key on the terminal emulator of the PC 110 (step 610), the PC 110 connects a communication path to the production-processing program of the firmware board 120 through the RS-232C communication line using the terminal emulator. In response to the input of the return key, the production-processing program outputs a command input prompt to display "ITCMON>" on a display screen of the PC 110 (step 620). In response to the display of "ITCMON>" on the display screen of the PC 110, the user may enter a SIGLO command, i.e., a command for transmitting the file for production. If so, "ITCMON>SIGLO" is displayed on the screen of the PC 110 (step 630). The PC 110, in response to the input of the command for transmitting the file for production, transmits the production file to the firmware board 120 (step 640).

The production-processing program of the firmware board 120 receives the production file transmitted from the PC 110 and analyzes the header information of the received file for production (step 650), then stores the production file in the corresponding region of the flash memory 124 according to the analysis result (step 660). At this time, if the region of the flash

memory 124 where the production file is to be stored is the region where the production-processing program is stored - i.e., if the production-processing program itself is to be updated - the production-processing program copies its own program to the DRAM 126, jumps to the DRAM 126, then performs the production-process on the DRAM 126 (step 670). That is, the updating of the production-processing program is performed in the DRAM 126. If the updating of the production-processing program is completed, the updated production-processing program is stored back in the corresponding region of the flash memory 124 (step 680).

As described above, according to the present invention, a firmware program is upgraded by a production-processing program stored in a firmware board, and thus a separate hardware tool such as an EPROM writer is not required. In other words, the program of the firmware board can be upgraded only by the replacement of an operating program in the flash memory and not by the hardwired replacement of the EPROM. Thus, the program of the firmware board can be upgraded without turning off the power supply to the firmware board. Moreover, as the upgrading of the firmware program can be achieved independently from an upper system where the firmware board is packaged and the upgrading is performed only for the module that requires the upgrading, the inconvenience of replacing the EPROM, which stores the programs of the whole firmware board, can be prevented.

Further, the present invention has advantages in that module execution files can be freely positioned in the desired region of the flash memory using the tool (SIGX2B) by

converting the execution files prepared by a programmer into the production files using the .INI file for specifying the configuration of production. Also, the present invention have advantage of using the firmware board without regards to the operation type of the firmware board since the booting status and the booting address can be designated in the event that the
5 firmware board operates not only directly on the flash memory but also through booting the DRAM.

The previous description of the preferred embodiments is provided to enable any person skilled in the art to make or use the present invention. The various modifications to these embodiments will be readily apparent to those skilled in the art. Thus, the present
10 invention is not intended to be limited to the embodiments shown herein but is to be accorded the widest scope consistent with the principles and novel features disclosed herein. Therefore, it is intended that the present invention not be limited to the particular embodiment disclosed as the best mode contemplated for carrying out the present invention, but that the present invention includes all embodiments falling within the scope of the appended claims.

WHAT IS CLAIMED IS:

1. An apparatus for upgrading the program stored in a firmware board comprising:

a host computer for converting an execution file prepared by an operator into a file for a production;

5 a flash memory disposed in the firmware board for storing a production-processing program; and,

a personal computer (PC) for receiving the production file downloaded from the host computer and for storing the downloaded file in a corresponding region of the flash memory.

2. The apparatus as claimed in Claim 1, further comprising an RS232C line for
10 connecting the PC to the firmware board.

3. The apparatus as claimed in Claim 1, wherein the host computer, prior to the creation of the file for production, attaches information relating to a storage address of the flash memory, a compression state, and a booting state for the production file.

4. The apparatus as claimed in Claim 1, wherein the PC transmits the production file to
15 the flash memory when a transmission command is inputted thereto.

5. The apparatus as claimed in Claim 1, wherein the PC stores the production file in the flash memory using the production-process program in the flash memory.

6. The apparatus as claimed in Claim 5, further comprising a DRAM for storing a copy of the production-process program from the flash memory when upgrading the production-processing program so that the upgrading can be performed in the DRAM.

7. The apparatus as claimed in Claim 6, wherein the upgraded production-process program in the DRAM is transferred back to the flash memory.

8. An apparatus for upgrading the operation system firmware of a personal computer system by downloading an updated firmware to acquire new capabilities, comprising:

a host computer for converting an execution file prepared by an operator into said updated firmware;

at least one personal computer coupled to said host computer for receiving said new firmware downloaded from said host computer;

a firmware board having:

a communication interface means connected for communicating with said personal computer and for transferring data between said personal computer and said firmware board;

a first memory means coupled to said communication means for storing a boot program, operating codes, and said operating system firmware; and,

wherein said personal computer further operable for storing said updated firmware

5 downloaded from said host computer in a corresponding region of said first memory means.

9. The apparatus as claimed in Claim 8, further comprising a second memory means coupled to said first memory means for storing a copy of information stored in said first memory means to be replaced with said updated firmware.

10. The apparatus as claimed in Claim 9, wherein said replaced updated firmware in said second means is transferred back to the corresponding region of said first memory means.

11. The apparatus as claimed in Claim 10, wherein said second memory means comprises a Dynamic Random Access Memory (DRAM) or a Static Random Access Memory (SRAM).

12. The apparatus as claimed in Claim 8, wherein said first memory means comprises a flash memory, and wherein said communication means comprises an RS232C line.

13. The apparatus as claimed in Claim 8, wherein said host computer further operable for attaching a storage address information of said first memory means to said updated firmware.

14. A method for upgrading the program of a firmware board comprising the steps of:

providing a flash memory in the firmware board for storing a production-processing

5 program;

creating, by a host computer, a file for a production by converting an execution file prepared in advance into the file for production;

receiving the production file, by a personal computer (PC), downloaded from the host computer; and,

5 storing the production file in the corresponding region of the flash memory.

15. The method as claimed in Claim 14, wherein the production file includes a header portion containing information relating to a storage address of the flash memory, a compression state, and a booting state for the production file.

10 16. The method as claimed in Claim 14, wherein the PC transmits the file for production to the flash memory when a transmission command is inputted thereto.

17. The method as claimed in Claim 14, wherein the method further comprising the step of duplicating the production-process program in an externally connected DRAM/SRAM while
15 upgrading the production-processing program in the DRAM/SRAM.

18. The method as claimed in Claim 17, wherein the upgraded program in the DRAM/SRAM is transferred to the corresponding region of the flash memory.

19. A method for upgrading operation system firmware of a personal computer system, comprising the steps of:

providing an updated firmware in a host computer to provide new capabilities;

downloading said updated firmware from said host computer to at least one personal

5 computer system;

establishing a communication connection between said personal computer system and a first memory of said personal computer system;

transferring said updated firmware to said first memory of said personal computer system by way of said communication connection; and,

10 storing said updated firmware in said first memory;

20. The method as claimed in Claim 19, wherein said host computer further performs the step of attaching a storage address information of said first memory to said updated firmware.

21. The method as claimed in Claim 19, further including the step of duplicating the information stored in said first memory in a second memory, coupled to said first memory, so that the duplicated information can be replaced with said updated firmware in said second memory.

22. The apparatus as claimed in Claim 21, wherein said updated firmware in said second memory is transferred back to the corresponding region of said first memory.

23. The method as claimed in Claim 19, wherein said second memory comprises a Dynamic Random Access memory (DRAM) or a Static Random Access memory (SRAM).

24. The method as claimed in Claim 19, wherein said first memory comprises a flash memory, and wherein said communication connection comprises an RS232C line.

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ABSTRACT OF THE DISCLOSURE

Disclosed is an apparatus and method for upgrading the program of a firmware board.

In the embodiment of the apparatus, a flash memory is provided on the firmware board and stores a program for production therein. A host computer produces the file for production by

5 converting an execution file prepared in advance into the file for production. A personal computer (PC) receives the file for production downloaded from the host computer and stores the downloaded file for production in the corresponding region of the flash memory. The PC stores the file for production in the corresponding region of the flash memory using the production-processing program of the firmware board.

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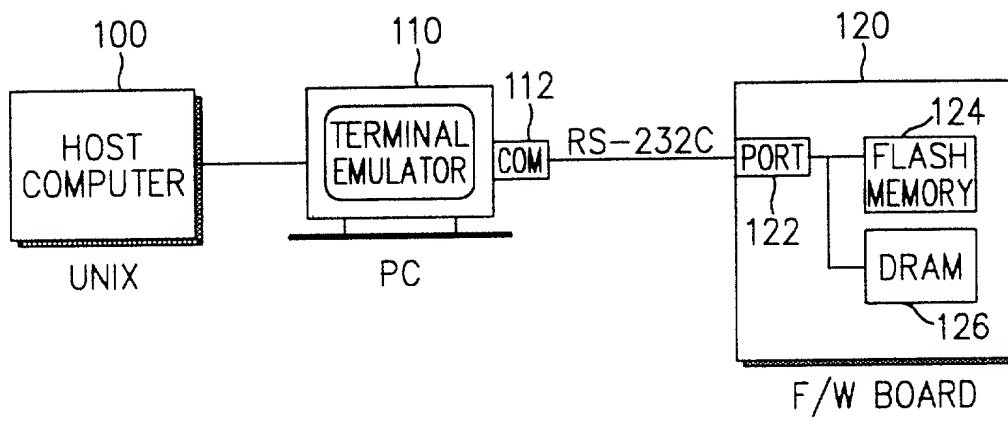


FIG. 1

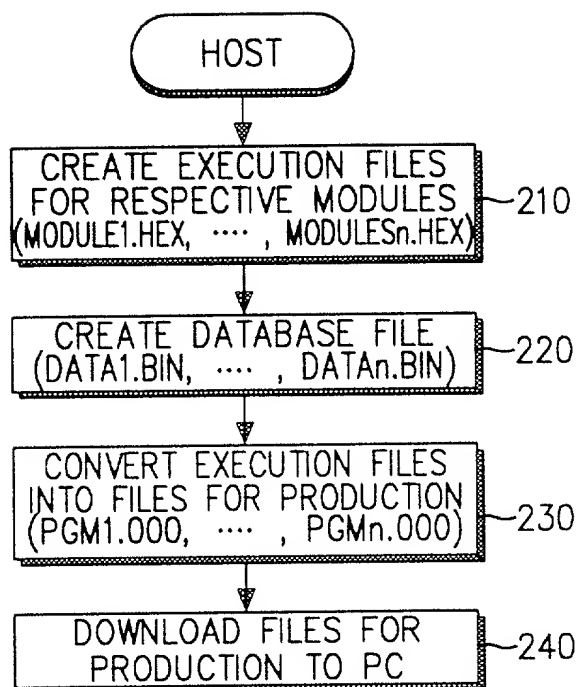


FIG. 2

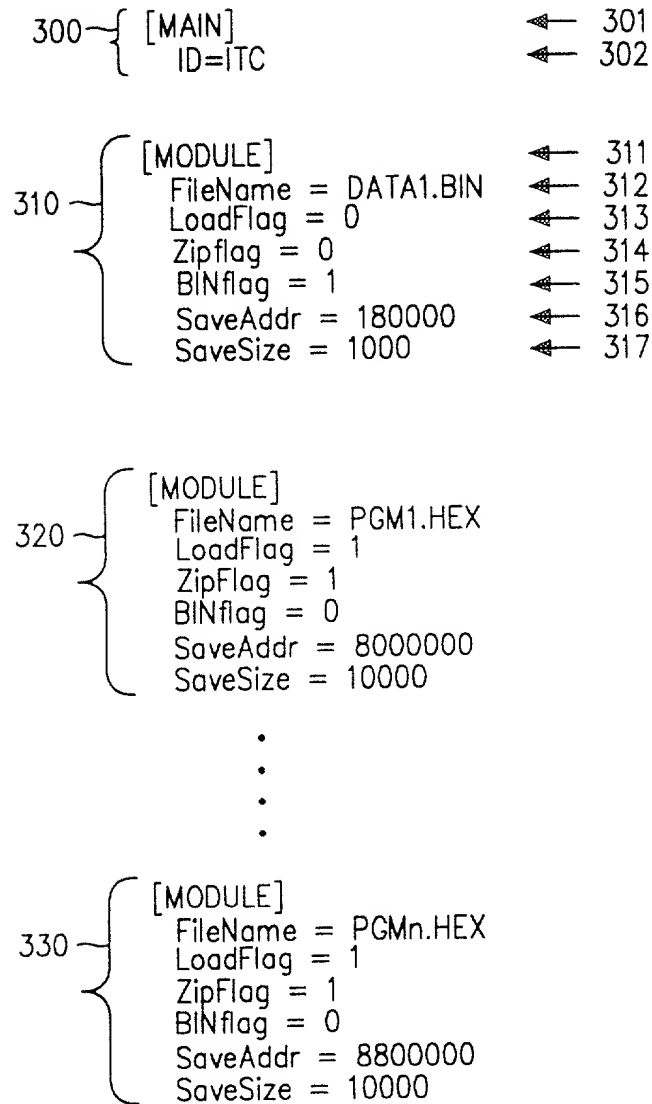


FIG. 3

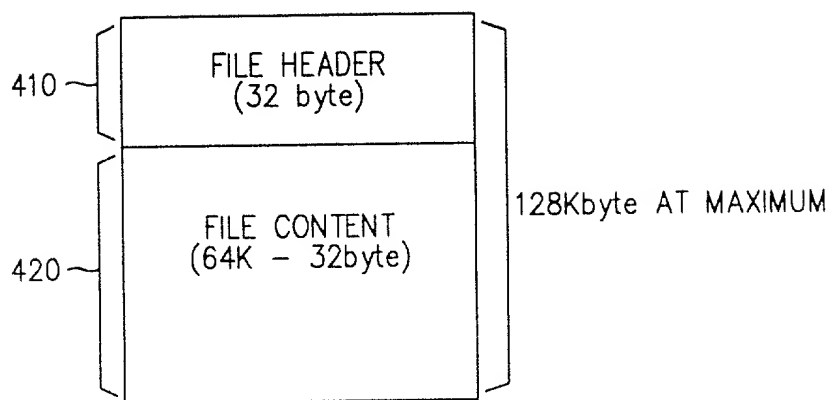


FIG. 4

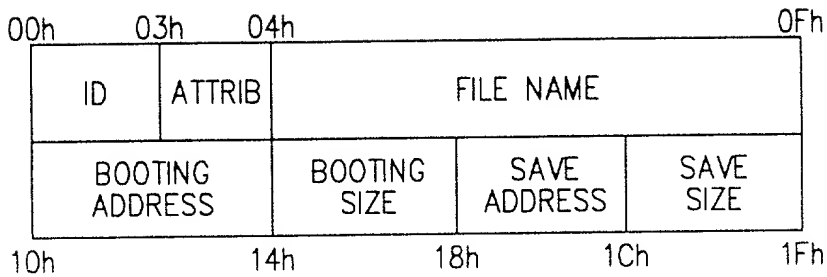


FIG. 5

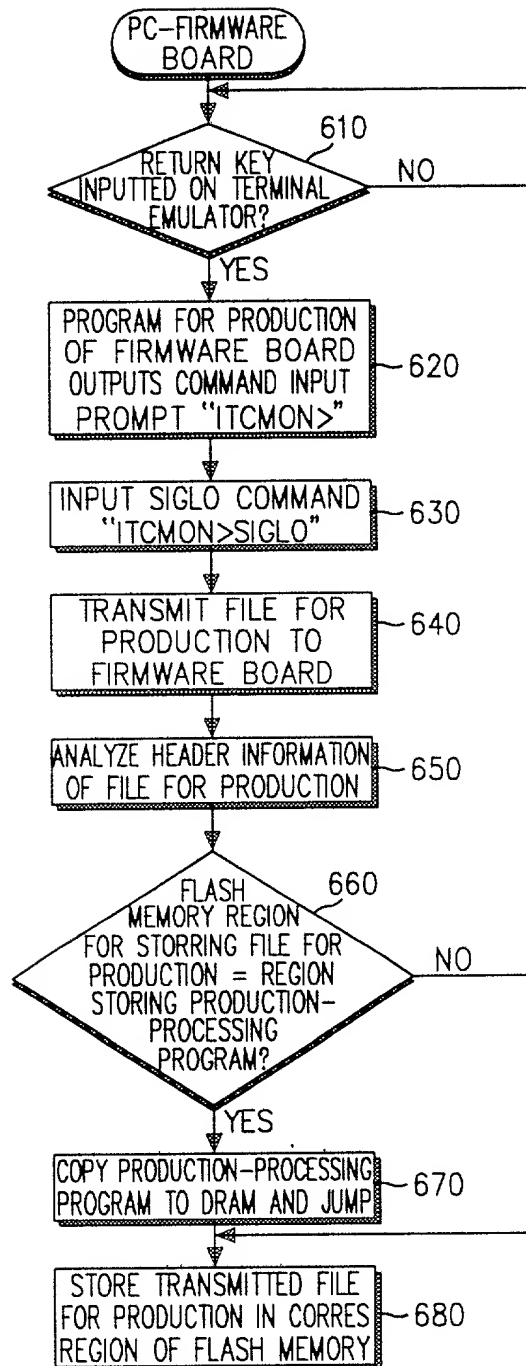


FIG. 6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: JUNG-GI KIM

Serial No: *Not yet assigned.*

Examiner: *Not yet assigned.*

Filed: June 9, 2000

Group: *Not yet assigned.*

For: PROGRAM UPGRADING APPARATUS AND METHOD FOR FIRMWARE BOARD

TRANSMITTAL OF DECLARATION

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

This transmittal accompanies a fax copy of Declaration with the signature by the inventors, for the above-captioned application. A substitute Declaration with the inventors' signature will be filed upon receipt of the Serial No. for the above-captioned application.

Respectfully submitted,



Steve S. Cha

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Date: June 9, 2000

006090 "54376560

PTO/SB/02B

Customer No.:022491

Docket No.: P2008

DECLARATION AND POWER OF ATTORNEY

As a below named inventor (s), I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: APPARATUS AND METHOD OF UPGRADING PROGRAM OF FIRMWARE BOARD

the specification of which is attached hereto unless the following box is checked:

☐ was filed on _____ as United States Application Number or PCT International Application Number _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulation, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor=s certificate listed below and have also identified below any foreign application for patent or inventor=s certificate having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

<u>1999-21353</u> (Number)	<u>Republic of Korea</u> (Country)	<u>09/06/1999</u> (Day/Month/Year Filed)	Priority Claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of the Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulation, § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

_____ (Application Number)	_____ (Filing Date)	_____ (Status - patented, pending, abandoned)
_____ (Application Number)	_____ (Filing Date)	_____ (Status - patented, pending, abandoned)

POWER OF ATTORNEY: I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: **Steve S. Cha**, Registration No. 44,069; **Howard S. Reiter**, Registration No. 20,394.

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I (we) hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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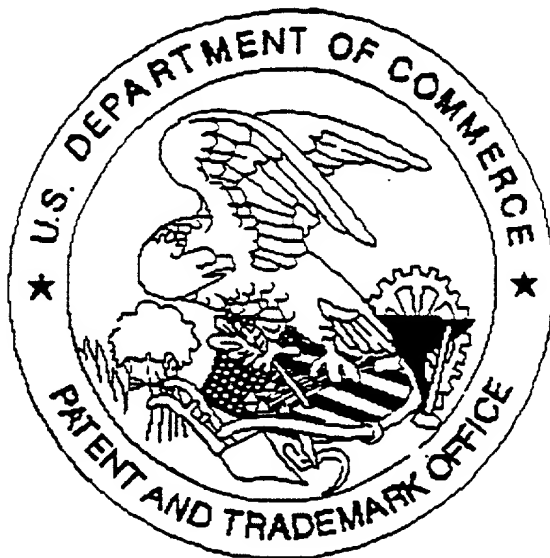
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